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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Chun JiangApplication No./Patent No.: Not yet assigned Filed/Issue Date: _____Entitled: Zero Power Memory Cell With Reduced Threshold VoltageLattice Semiconductor Corporation, a Delaware Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or2. ☐ an assignee of less than the entire right, title and interest.The extent (by percentage) of its ownership interest is _____ %
in the patent application/patent identified above by virtue of either:A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 012832, Frame 0920, or for which a copy thereof is attached.**OR**B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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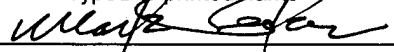
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

7/24/03
Date503-268-8629

Telephone number

Mark L. Becker

Typed or printed name



Signature

Associate General Counsel, IP

Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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